

## VERSION SHOWING THE AMENDMENTS TO THE CLAIMS

### IN THE CLAIMS

Amend the claims as follows:

1. (Currently amended) An organic logic gate comprising:  
a circuit having an output and comprising at least one charging field effect transistor (charging FET) having source, drain and gate electrodes and at least one switching field effect transistor (switching FET), the charging FET having at least one gate electrode, a source electrode and a drain electrode, the drain-source electrodes of the charging and switching transistors being arranged to be coupled in series between a voltage source and a reference potential such that ~~characterized in that~~ the gate electrode of the charging FET is not connected via an electrical line directly to the a ~~voltage source, to the reference potential or to the output.~~
  
2. (Currently amended). The organic logic gate as claimed in claim 1 ~~characterized in that~~ wherein the gate electrode of the charging FET is capacitively coupled to the source electrode of the charging FET.
  
3. (Currently amended). The organic logic gate as claimed in Claim 2 ~~characterized in that~~ wherein the capacitive coupling is achieved by ~~means of~~ the gate electrode of the charging FET overlapping the source electrode of the charging FET.

4. (Currently Amended) The organic logic gate as claimed in Claim 1[[,]]  
~~characterized in that~~ wherein the gate electrode of the charging FET is resistively  
coupled to the source electrode of the charging FET.

5. (Currently Amended). The organic logic gate as claimed in Claim 1[[,]]  
~~characterized in that~~ wherein the gate electrode of the charging FET is capacitively  
coupled to the drain electrode of the charging FET.

6. (Currently amended). The organic logic gate as claimed in claim 5[[,]]  
~~characterized in that~~ wherein the capacitive coupling is achieved by ~~means of~~ the drain  
electrode overlapping the gate electrode of the charging FET.

7. (Currently Amended). The organic logic gate as claimed in Claim 1[[,]]  
~~characterized in that~~ wherein the gate electrode of the charging FET is resistively  
coupled to the drain electrode of the charging FET.

8. (Currently Amended) The organic logic gate as claimed in Claim 1[[,]]  
~~characterized in that~~ wherein the organic logic gate is constructed without plated-  
through holes.